Rocktech Displays Limited



Module P/N: RK080IX03DH	•
-------------------------	---

Version: 1.0

Description: 8.0 inch TFT 1024*768 pixels with

LED backlight, All viewing angle,

1000 nits brightness

TEL: <u>0086-755-26065260</u>

Fax: 0086-755-26065261

E-mail: Sales@rocktech.com.hk

Web: www.rocktech.com.hk



Revision History

Date	Rev.	Page	Description
2024-08-29	1.0	All	



CONTENTS

- GENERAL FEATURES
- ABSOLUTE MAXIMUM RATINGS
- ELECTRICAL SPECIFICATIONS
- OPTICAL SPECIFICATIONS
- BLOCK DIAGRAM
- PIN DESCRIPTION
- TIMING CHARACTERISTICS
- OUTLINE DIMENSION
- RELIABILITY AND INSPECTION STANDARD
- PRECAUTIONS



1. General Features

Item	Spec	Remark
Display Mode	Normally Black Transmissive	
Viewing Direction	FREE	IPS
Input Signals	LVDS 6/8 bits	
Outside Dimensions(mm)	182.90(W) x141.00(H) x5.5(D)	
Active Area(mm)	162.05(W)×121.54(H)	
Number of Pixels	1024(RGB)×768	
Dot Pitch(mm)	0.1582(W) ×0.1582(H)	
Pixel Arrangement	RGB Vertical stripes	
Drive IC	NT51008M+NT39212F	



2. Absolute Maximum Ratings

The following are maximum values which, if exceeded may cause operation or damage to the unit.

ITEM	Sym.	Min.	Тур.	Max.	Unit	Remark
	VDD	-0.3	-	5.0	V	
Dower for Circuit Driving	AVDD	6.5	-	13.5	V	
Power for Circuit Driving	VGH	-0.3	-	40.0	V	
	VGL	-20.0	-	0.3	V	
Storage Humidity	H _{ST}	10	-		%RH	
Storage Temperature	T _{ST}	-30	-	80	$^{\circ}\mathbb{C}$	At
Operating Ambient Humidity	H _{OP}	10	-		%RH	25±5℃
Operating Ambient temperature	T _{OP}	-20	-	70	$^{\circ}\!\mathbb{C}$	



3. Electrical Specification

3.1 Driving TFT LCD Panel

Item	Sym.	Min	Тур.	Max	Unit	Note
Supply Voltage	VDD	3.0	3.3	3.6	V	
	VGH	18.6	18.9	19.2	V	
	VGL	-8.1	-7.8	-7.5	V	
	AVDD	9.8	10.0	10.2	V	
	VCOM	2.6	3.6	4.6	V	

3.2 Driving LED Backlight

Item	Sym.	Min	Тур.	Max	Unit	Note
Backlight driving voltage	VF	-	9.3	1	V	
Backlight driving current	lF	380	420	460	mA	
Backlight Power Consumption	WBL	-	3906	1	mW	
Life Time	-	-	50,000	-		Note 1

Note 1: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.



4.Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of Φ and θ equal to 0° .

Mana.	Curre		Values		11:4	Note	
Item	Sym.	Min.	Тур.	Max.	Unit		
1)Contrast Ratio	C/R	600	800	1		FIG.1	
2)Module Luminance	L	900	1000	1	cd/m ²	FIG.1	
3)Response time	Tr+Tf	-	25	50	ms	FIG.2	
	θτ	80	85	-			
4)\/ioving Angle	θв	80	85	-	Dograda	FIC 2	
4)Viewing Angle	θL	80	85	-	Degree	FIG.3	
	θ_{R}	80	85	-			
	Wx	0.248	0.288	0.328			
	Wy	0.286	0.326	0.366			
	Rx	-	-	-			
E)Chromoticity	Ry	-	-	-			
5)Chromaticity	Gx	-	-	-			
	Gy	-	-	-			
	Вх	-	-	-			
	Ву	-	-	-			



♦ Measurement System

Notes:

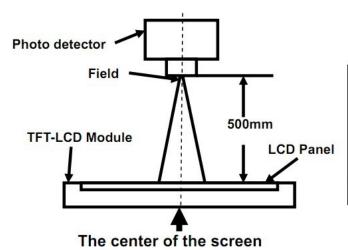
1. Contrast Ratio(CR) is defined mathematically as:

Surface Luminance with all white pixels

Contrast Ratio = -----Surface Luminance with all black pixels

- 2. Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white. For more information see FIG 1.
- 3. Response time is the time required for the display to transition from white to black (Rising Time, Tr) and from black to white (Falling Time, Tf). For additional information see FIG 2.
- 4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

FIG. 1 Optical Characteristic Measurement Equipment and Method



Item	Photo detector	Field
Contrast Ratio		
Luminance	CD 24	1°
Chromaticity	SR-3A	1
Lum Uniformity		
Response Time	BM-7A	2°



FIG. 2 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".

Response Time = Rising Time(Tr) + Falling Time(Tf)

- Rising Time(Tr): Full White 90% → Full White 10% Transmittance.
- Falling Time(Tf): Full White 10% → Full White 90% Transmittance.

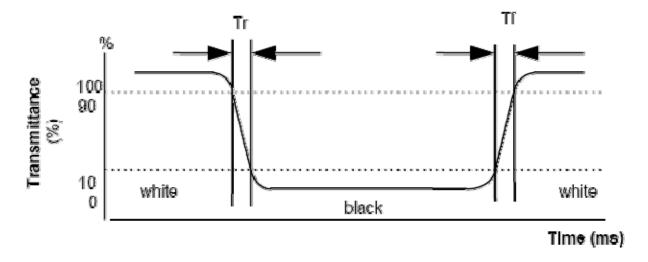
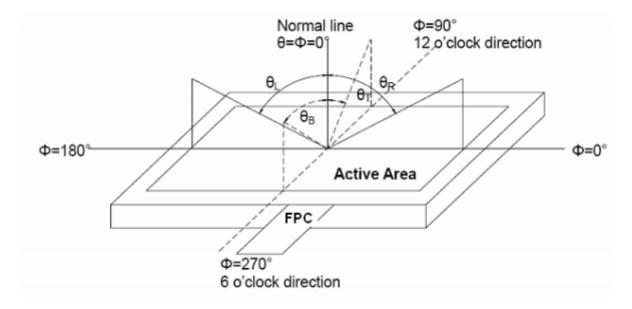


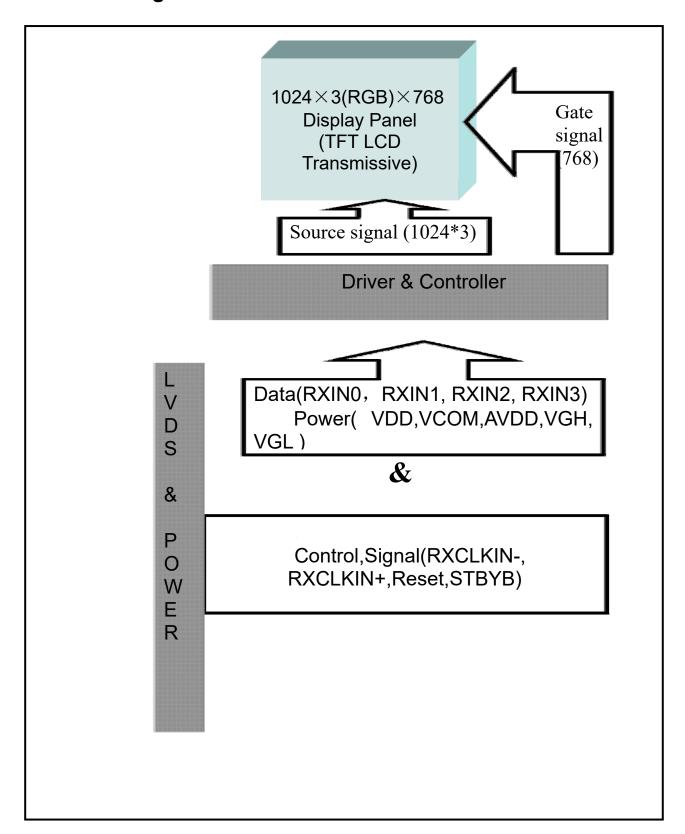
FIG. 3 The definition of Viewing Angle

Use Fig. 1(Test Procedure) under Measurement System to measure the contrast from the measuring direction specified by the conditions as the following figure.





5. Block Diagram





6.Pin Description

6.1 TFT LCD Panel

FPC Connector is used for the module electronics interface. The recommended model is FH12A-40S-0.5SH manufactured by Hirose.

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	Р	Common Voltage	
2	VDD	Р	Power Voltage for digital circuit	
3	VDD	Р	Power Voltage for digital circuit	
4	NC		No connection	
5	Reset	1	Global reset pin	
6	STBYB	I	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z	
7	GND	Р	Ground	
8	RXIN0-	I	- LVDS differential data input	
9	RXIN0+	I	+ LVDS differential data input	
10	GND	Р	Ground	
11	RXIN1-	I	- LVDS differential data input	
12	RXIN1+	I	+ LVDS differential data input	
13	GND	Р	Ground	
14	RXIN2-	I	- LVDS differential data input	
15	RXIN2+	I	+ LVDS differential data input	
16	GND	Р	Ground	
17	RXCLKIN-	I	- LVDS differential clock input	
18	RXCLKIN+	I	+ LVDS differential clock input	
19	GND	Р	Ground	
20	RXIN3-	1	- LVDS differential data input	
21	RXIN3+	I	+ LVDS differential data input	
22	GND	Р	Ground	
23	NC		No connection	



			,	
24	NC		No connection	
25	GND	Р	Ground	
26	NC		No connection	
27	DIMO	0	Backlight CABC controller signal output	
28	SELB	I	6bit/8bit mode select	Note1
29	AVDD	Р	Power for Analog Circuit	
30	GND	Р	Ground	
31	LED-	Р	LED Cathode	
32	LED-	Р	LED Cathode	
33	L/R	I	Horizontal inversion	Note3
34	U/D	I	Vertical inversion	Note3
35	VGL	Р	Gate OFF Voltage	
36	CABCEN1	I	CABC H/W enable	Note2
37	CABCEN0	I	CABC H/W enable	Note2
38	VGH	Р	Gate ON Voltage	
39	LED+	Р	LED Anode	
40	LED+	Р	LED Anode	

I: input, O: output, P: Power

Note1: If LVDS input data is 6 bits ,SELB must be set to High;

If LVDS input data is 8 bits ,SELB must be set to Low.

Note2: When CABC_EN="00", CABC OFF.

When CABC_EN="01", user interface image.

When CABC_EN="10", still picture. When CABC_EN="11", moving image.

When CABC off, don't connect DIMO, else connect it to backlight.

Note3: When L/R="0", set right to left scan direction.

When L/R="1", set left to right scan direction. When U/D="0", set top to bottom scan direction. When U/D="1", set bottom to top scan direction.

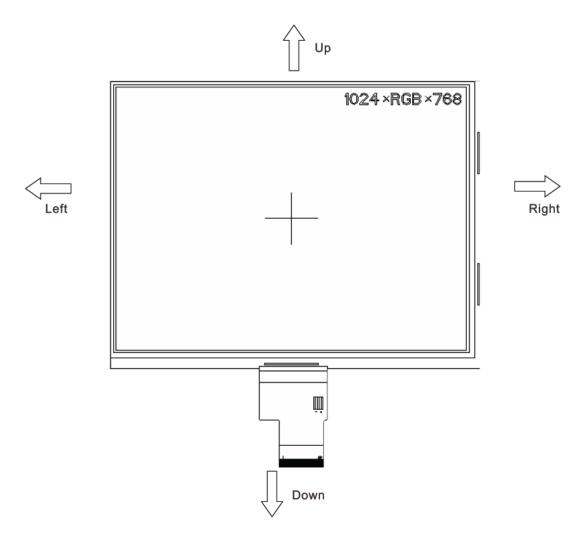


6.2 U/D R/L Function Description

Scan Con	trol Input	Seanning Direction
UPDN	SHLR	Scanning Direction
GND	VDD	Up to Down, Left to Right
VDD	GND	Down to Up, Right to Left
GND	GND	Up to Down, Right to Left
VDD	VDD	Down to Up, Left to Right

Note: Definition of scanning direction.

Refer to the figure as below:



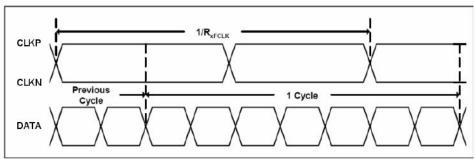


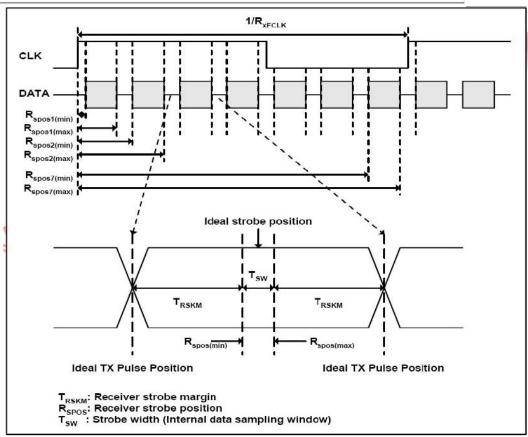
7. Timing Characteristics

7.1 AC electrical Characteristics

Parameter	Symbol		Values	Unit	Remark	
raiailletei	Symbol	Min.	Тур.	Max.	Oilit	Kemark
Clock frequency	R _{xFCLK}	20	-	71	MHz	
Input data skew margin	T _{RSKM}	500	-	-	ps	
Clock high time	T _{LVCH}	-	4/(7* R _{xFCLK})	-	ns	
Clock low time	T _{LVCL}	-	3/(7* R _{xFCLK})	-	ns	

Input Clock and Data Timing Diagram

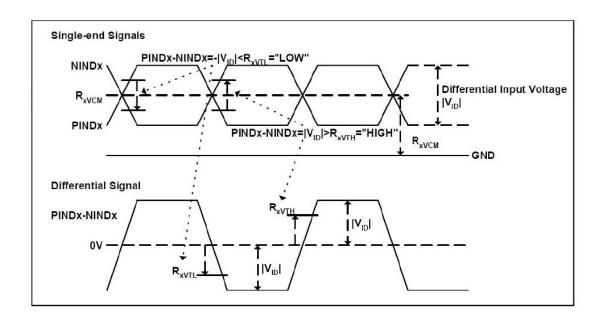






7.2 DC electrical Characteristics

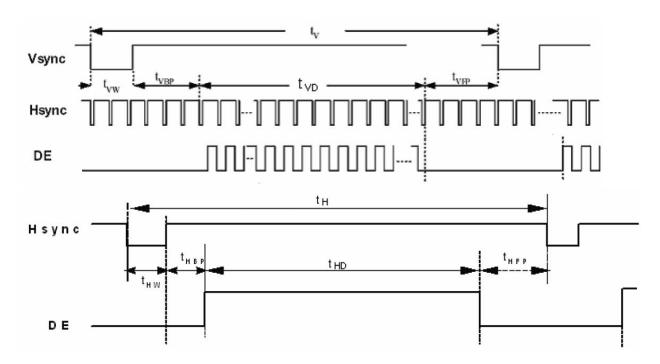
Parameter	Symbol	Values			Unit	Remark	
1 212110101		Min.	Тур.	Max.		Homan	
Differential input high Threshold voltage	R _{xVTH}	-	-	+0.1	V	R _{XVCM} =1.2V	
Differential input low Threshold voltage	R _{xVTL}	-0.1	-	-	V	TXXVCM-1.2V	
Input voltage range (singled-end)	R _{xVIN}	0	_	2.4	٧		
Differential input common mode voltage	R _{xVCM}	V _{ID} /2	2	2.4- V _{ID} /2	V		
Differential voltage	V _{ID}	0.2	-	0.6	V		
Differential input leakage current	RV_{xliz}	-10	-	+10	uA		





7.3 Timing

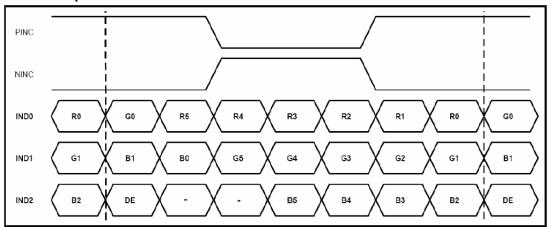
Item	Symbol		Values	Unit	Remark	
item	Symbol	Min.	Тур.	Max.	Offic	Kemark
Clock Frequency	fclk	52	65	71	MHz	Frame rate =60Hz
Horizontal display area	thd		1024		DCLK	
HS period time	th	1114	1344	1400	DCLK	
HS Blanking	thb+thfp	90	320	376	DCLK	
Vertical display area	tvd		768		Н	
VS period time	tv	778	806	845	Н	
VS Blanking	tvb+tvfp	10	38	77	Н	



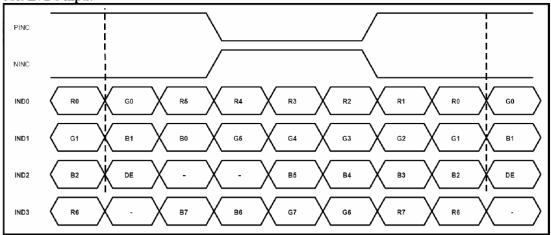


7.4 Data Input Format

6bit LVDS input



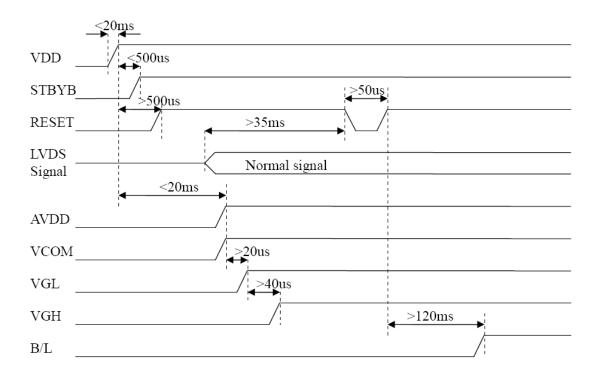
8bit LVDS input



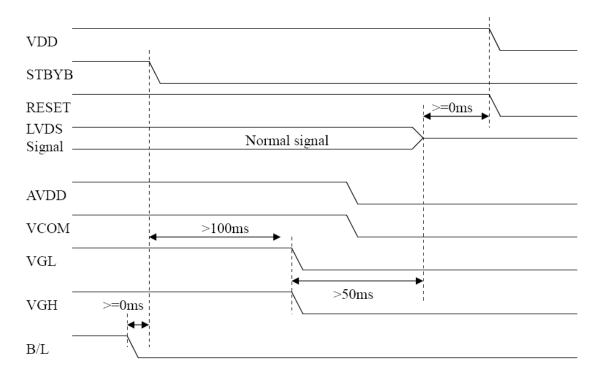
Note: Support DE timing mode only, SYNC mode not supported.



7.5 Power On/Off Sequence



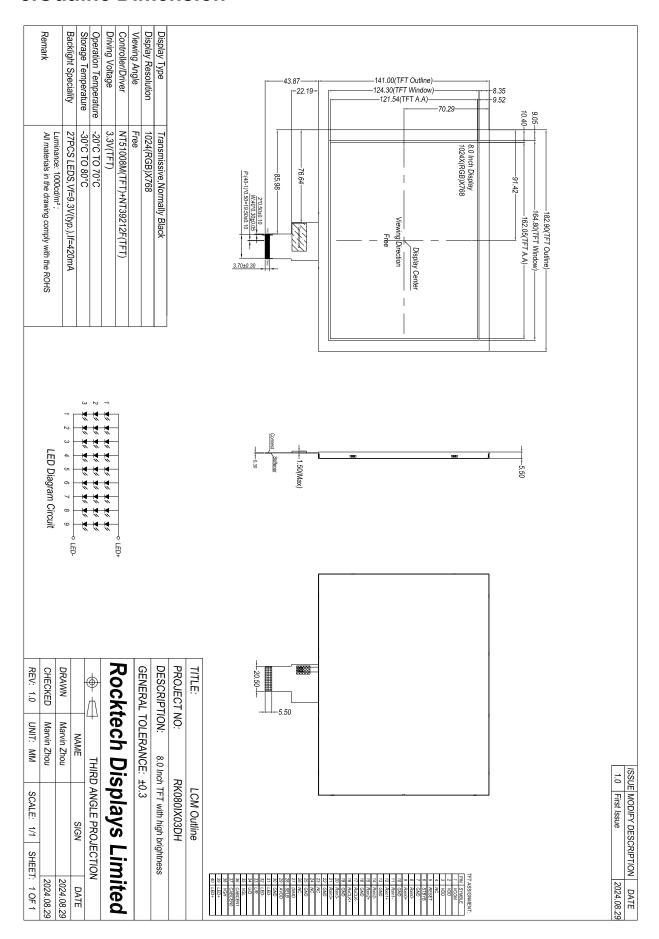
Power On



Power Off



8. Outline Dimension





9. Reliability and Inspection Standard

No.	Test Item		Test Conditions	Remark
1 High Temperature	Lligh Tomporature	Storage	80℃, 120Hr	Note
	Operation	70 ℃, 120 Hr	Note	
2 Low Temperature	Storage	-30℃, 120Hr	Note	
	Low remperature	Operation	-20℃, 120Hr	Note
3	High Temperature and High Humidity		40℃, 90%RH, 120Hr	Note
4	Thermal Cycling Test(No operation)		-20℃ for 30min, 70℃ for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note
5	Vibration Test(No operation)		Frequency:10~55 HZ; Stroke:1.5 mm;Sweep:10HZ~55HZ~10HZ; 2hours for each direction of X, Y, Z(6 hours for total)	
6	Package Drop Test		Height:60 cm,1 corner, 3 edges, 6 surfaces	
7	Electro Static Discharge		± 2 KV,Human Body Mode, 100pF/1500 Ω	

Note:

- 1) Sample quantity for each test item is 5~10pcs.
- 2) Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.



10.PRECAUTIONS FOR USING LCD MODULES

Handing Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcohol

Do not scrub hard to avoid damaging the display surface.

- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solvents
 - Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.
- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal



connector.

- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

Storage Precautions

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature. If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- -Terminal electrode sections.