

Rocktech Displays Limited



Module P/N: RK070CU01

Version: 1.0

Description : 7.0 inch TFT 1024*600 pixels with
LED backlight ,All viewing angle
350 nits brightness

TEL: 0086-755-26065260

Fax: 0086-755-26065261

E-mail: Sales@rocktech.com.hk

Web: www.rocktech.com.hk

Revision History

Date	Rev.	Page	Description
2018-08-08	1.0	All	First Issue

CONTENTS

- GENERAL FEATURES
- ABSOLUTE MAXIMUM RATINGS
- ELECTRICAL SPECIFICATIONS
- OPTICAL SPECIFICATIONS
- BLOCK DIAGRAM
- PIN DESCRIPTION
- TIMING CHARACTERISTICS
- OUTLINE DIMENSION
- RELIABILITY AND INSPECTION STANDARD
- PRECAUTIONS

1. General Features

Item	Spec	Remark
Display Mode	Normally Black transmissive	
Viewing Direction	Free	IPS
Input Signals	LVDS 6/8 bits	
Outside Dimensions(mm)	164.90 (W) x100.00(H) x2.80(D)	
Active Area(mm)	154.21mm(W)×85.92mm(H)	
Number of Pixels	1024(RGB)×600	
Dot Pitch(mm)	0.1506mm (W) x 0.1432mm (H)	
Pixel Arrangement	RGB Vertical stripes	

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded may cause operation or damage to the unit.

ITEM	Sym.	Min.	Typ.	Max.	Unit	Remark
Power for Circuit Driving	VDD	-0.3	-	3.96	V	
	AVDD	-0.5		14.85	V	
	VGH	-0.3		40	V	
	VGL	-20		0.3	V	
Storage Humidity	H _{ST}	10	-		%RH	At 25±5°C
Storage Temperature	T _{ST}	-30	-	80	°C	
Operating Ambient Humidity	H _{OP}	10	-		%RH	
Operating Ambient temperature	T _{OP}	-20	-	70	°C	

3. Electrical Specification

3.1 Driving TFT LCD Panel

Item	Sym.	Min	Typ.	Max	Unit	Note
Supply Voltage	DVDD	3.0	3.3	3.6	V	
	VGH	17.0	18.0	19.0	V	
	VGL	-6.6	-6.0	-5.4	V	
	AVDD	9.4	9.6	9.8	V	
	VCOM	3.6	3.8	4.0	V	

3.2 Driving LED Backlight

Item	Sym.	Min	Typ.	Max	Unit	Note
Backlight driving voltage	V _F	-	9.6	-	V	
Backlight driving current	I _F	120	160	200	mA	
Backlight Power Consumption	W _{BL}	-	1536	-	mW	
Life Time	-	-	50,000	-		Note 1

Note 1: (Unless specified, the ambient temperature Ta=25°C)

Note 2: The recommended operating conditions refer to a range in which operation of this product is guaranteed. Should this range is exceeded, the operation cannot be guaranteed even if the values may be without the absolute maximum ratings.

Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

4. Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 °C. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

Item	Sym.	Values			Unit	Note
		Min.	Typ.	Max.		
1) Contrast Ratio	C/R	-	800	-		FIG.1
2) Module Luminance	L	300	350	-	cd/m ²	
3) Response time	Tr+Tf	-	35	-	ms	FIG.2
4) Viewing Angle	θ_T	-	85	-	Degree	FIG.3
	θ_B	-	85	-		
	θ_L	-	85	-		
	θ_R	-	85	-		
5) Chromaticity	Wx	0.27	0.31	0.35		
	Wy	0.29	0.33	0.37		
	Rx	-	-	-		
	Ry	-	-	-		
	Gx	-	-	-		
	Gy	-	-	-		
	Bx	-	-	-		
	By	-	-	-		

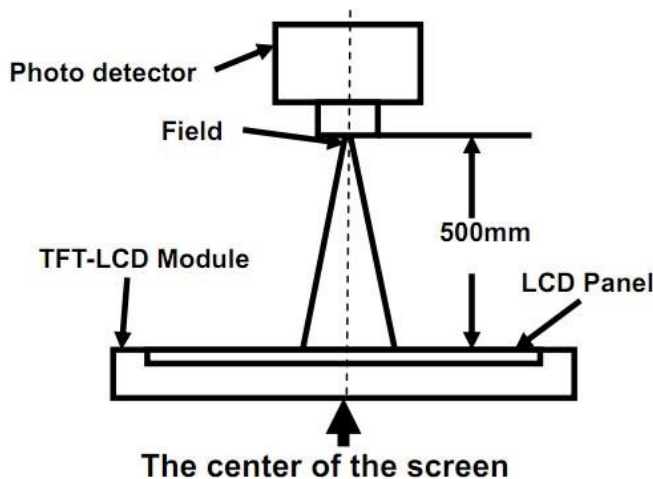
◆ Measurement System

Notes:

1. Contrast Ratio(CR) is defined mathematically as :

$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$
2. Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white. For more information see FIG 1.
3. Response time is the time required for the display to transition from white to black (Rising Time, Tr) and from black to white (Falling Time, Tf). For additional information see FIG 2.
4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

FIG. 1 Optical Characteristic Measurement Equipment and Method



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

FIG. 2 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for “black” and “white”.

$$\text{Response Time} = \text{Rising Time}(Tr) + \text{Falling Time}(Tf)$$

- Rising Time(Tr) : Full White 90% \rightarrow Full White 10% Transmittance.
- Falling Time(Tf) : Full White 10% \rightarrow Full White 90% Transmittance.

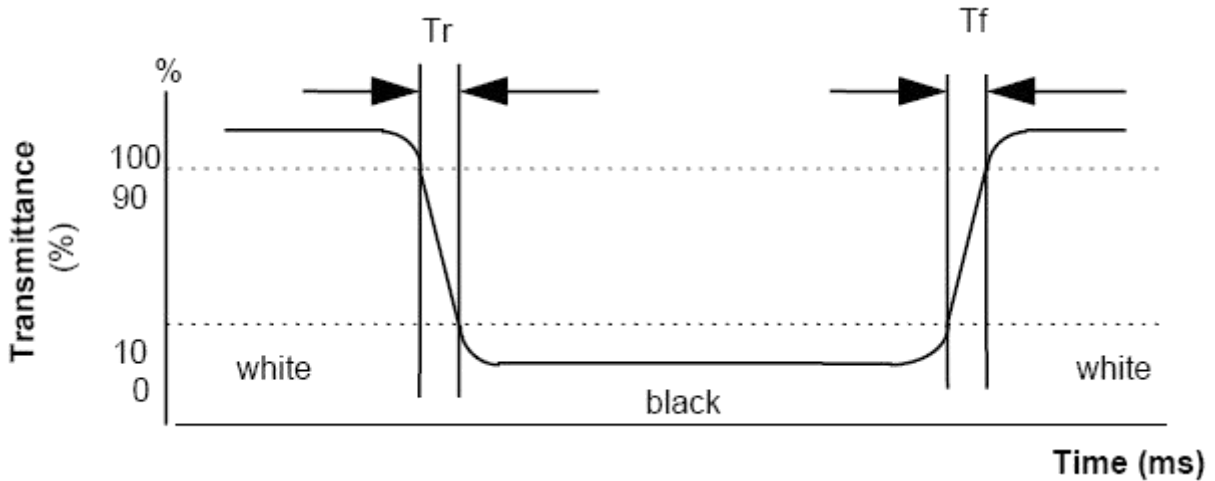
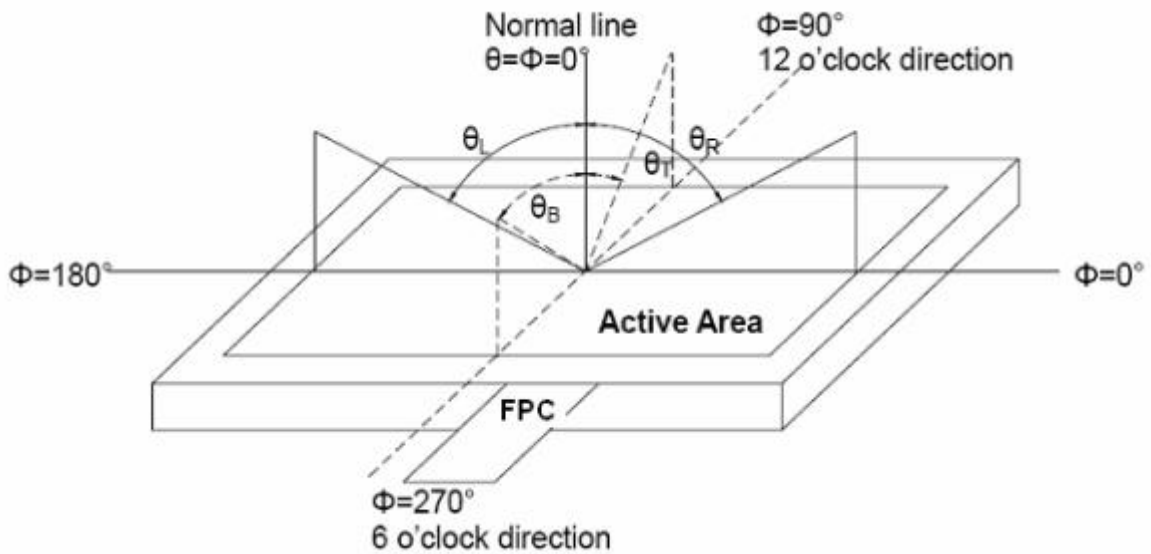
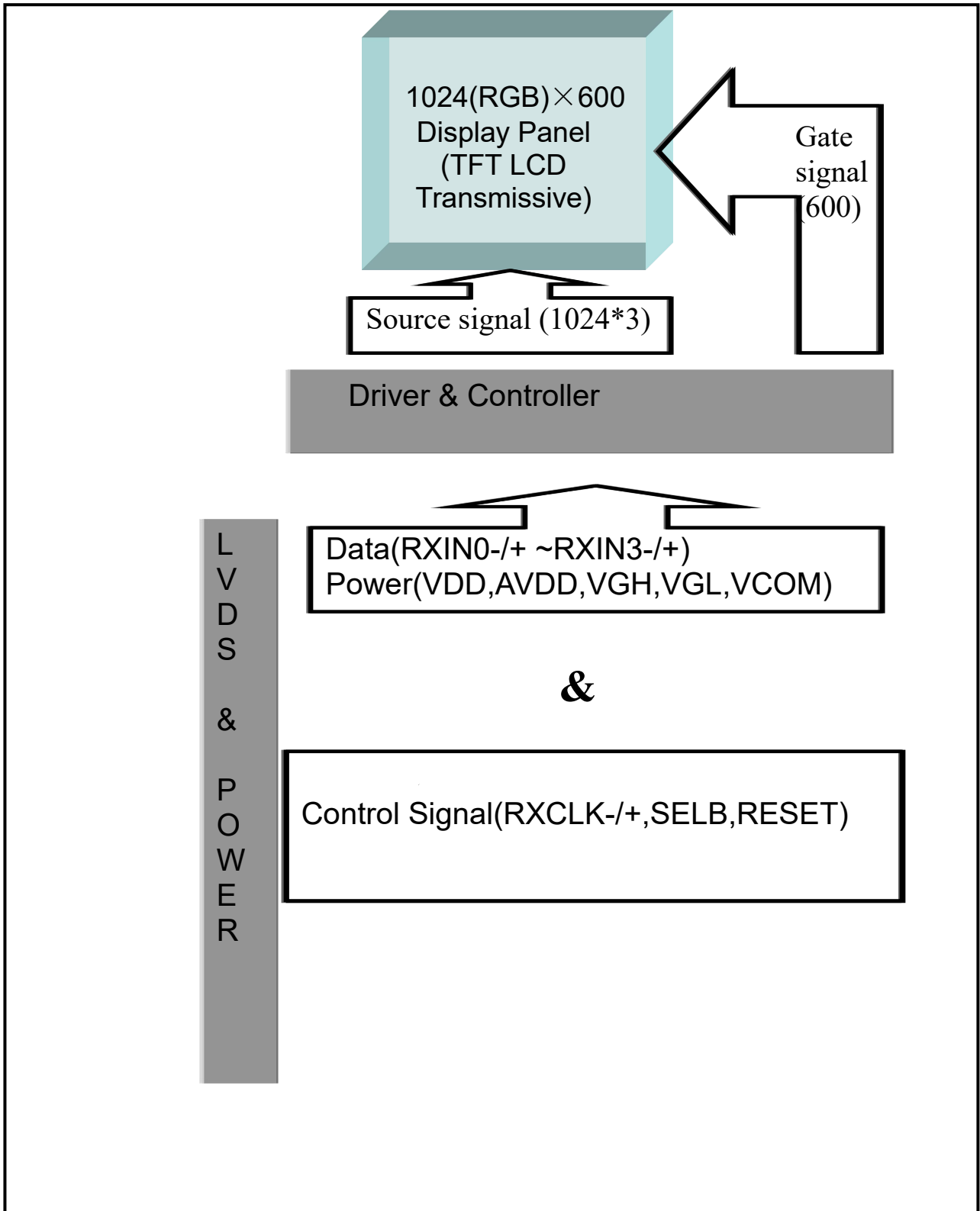


FIG. 3 The definition of Viewing Angle

Use Fig. 1 (Test Procedure) under Measurement System to measure the contrast from the measuring direction specified by the conditions as the following figure.



5. Block Diagram



6.Pin Description

6.1 TFT LCD Panel

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	P	Common Voltage	
2	VDD	P	Power Voltage for digital circuit	
3	VDD	P	Power Voltage for digital circuit	
4	NC	---	No connection	
5	Reset	I	Global reset pin	
6	STBYB	I	Standby mode, Normally pulled high STBYB = "1", normal operation STBYB = "0", timing controller, source driver will turn off, all output are High-Z	
7	GND	P	Ground	
8	RXIN0-	I	- LVDS differential data input	
9	RXIN0+	I	+ LVDS differential data input	
10	GND	P	Ground	
11	RXIN1-	I	- LVDS differential data input	
12	RXIN1+	I	+ LVDS differential data input	
13	GND	P	Ground	
14	RXIN2-	I	- LVDS differential data input	
15	RXIN2+	I	+ LVDS differential data input	
16	GND	P	Ground	
17	RXCLKIN-	I	- LVDS differential clock input	
18	RXCLKIN+	I	+ LVDS differential clock input	
19	GND	P	Ground	
20	RXIN3-	I	- LVDS differential data input	
21	RXIN3+	I	+ LVDS differential data input	
22	GND	P	Ground	
23	NC	---	No connection	

24	NC	---	No connection	
25	GND	P	Ground	
26	NC	---	No connection	
27	NC	-		
28	SELB	I	6bit/8bit mode select	Note1
29	AVDD	P	Power for Analog Circuit	
30	GND	P	Ground	
31	LED-	P	LED Cathode	
32	LED-	P	LED Cathode	
33	L/R	I	Horizontal inversion 80	Note3
34	U/D	I	Vertical inversion	Note3
35	VGL	P	Gate OFF Voltage	
36	NC			
37	NC			
38	VGH	P	Gate ON Voltage	
39	LED+	P	LED Anode	
40	LED+	P	LED Anode	

I: input, O: output, P: Power

Note1: If LVDS input data is 6 bits ,SELB must be set to High;
If LVDS input data is 8 bits ,SELB must be set to Low.

Note2: When CABC_EN="00", CABC OFF.

When CABC_EN="01", user interface image.

When CABC_EN="10", still picture.

When CABC_EN="11", moving image.

When CABC off, don't connect DIMO, else connect it to backlight.

Note3: When L/R="0", set right to left scan direction.

When L/R="1", set left to right scan direction.

When U/D="0", set top to bottom scan direction.

6.2 U/D R/L Function Description

Scan Control Input		Scanning Direction
UPDN	SHLR	
GND	VDD	Up to Down, Left to Right
VDD	GND	Down to Up, Right to Left
GND	GND	Up to Down, Right to Left
VDD	VDD	Down to Up, Left to Right

7. Timing Characteristics

7.1. Input Setup Timing Setting

7.1.1. Parallel RGB Timing Characteristics

DE mode

DE mode					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
DCLK frequency @Frame rate=60hz	fclk	40.8	51.2	67.2	Mhz
Horizontal display area	thd	1024			DCLK
HSYNC period time	th	1114	1344	1400	DCLK
HSYNC blanking	thb+thfp	90	320	376	DCLK
Vertical display area	tvd	600			H
VSYNC period time	tv	610	635	800	H
VSYNC blanking	tvb+tvfp	10	35	200	H

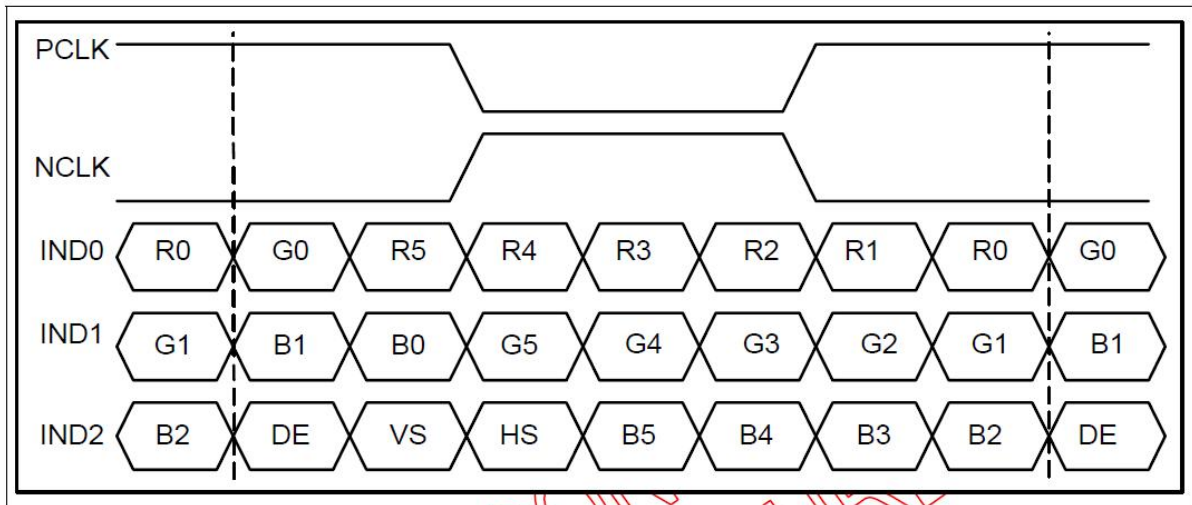
HV mode(1)

HV mode					
Horizontal input timing					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Horizontal display area	thd	1024			DCLK
DCLK frequency@ Frame rate=60hz	fclk	44.9	51.2	63	Mhz
1 Horizontal Line	th	1200	1344	1400	DCLK
HSYNC pulse width	thpw	Min.	1		
		Typ.	—		
		Max.	140		
HSYNC back porch	thbp	160	160	160	
HSYNC front porch	thfp	16	160	216	

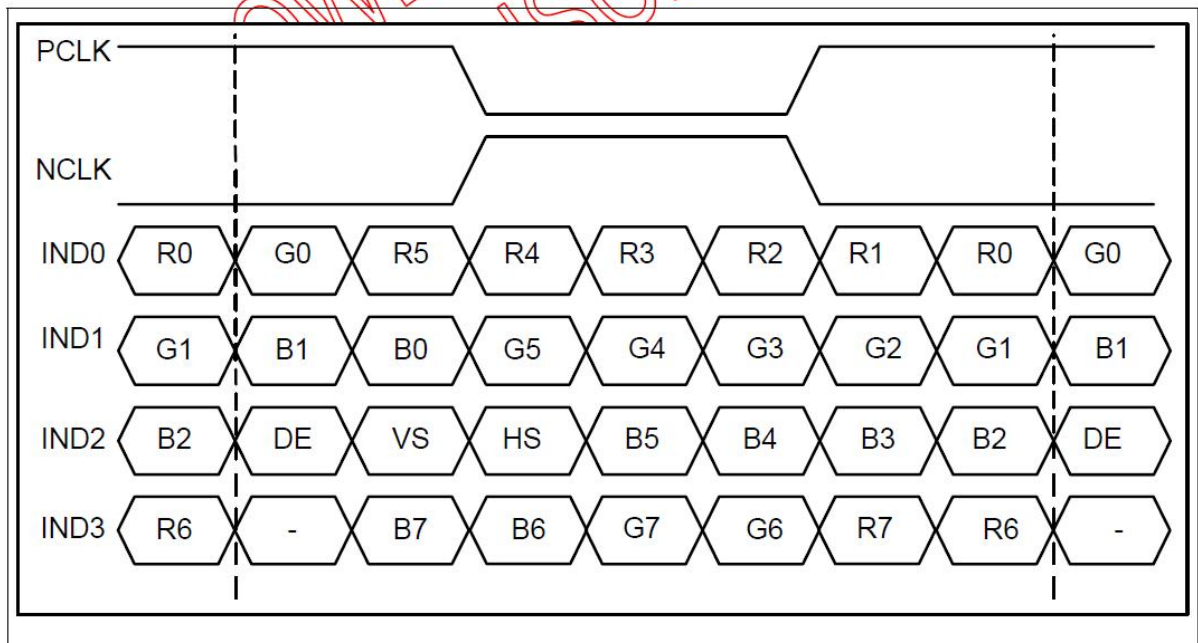
HV mode(2)

Vertical input timing					
Parameter	Symbol	Value			Unit
		Min.	Typ.	Max.	
Vertical display area	tvd	600			H
VSYNC period time	tv	624	635	750	H
VSYNC pulse width	tvpw	1	—	20	H
VSYNC back porch	tvb	23	23	23	H
VSYNC front porch	tvfp	1	12	127	H

7.1.2. Data Input Format



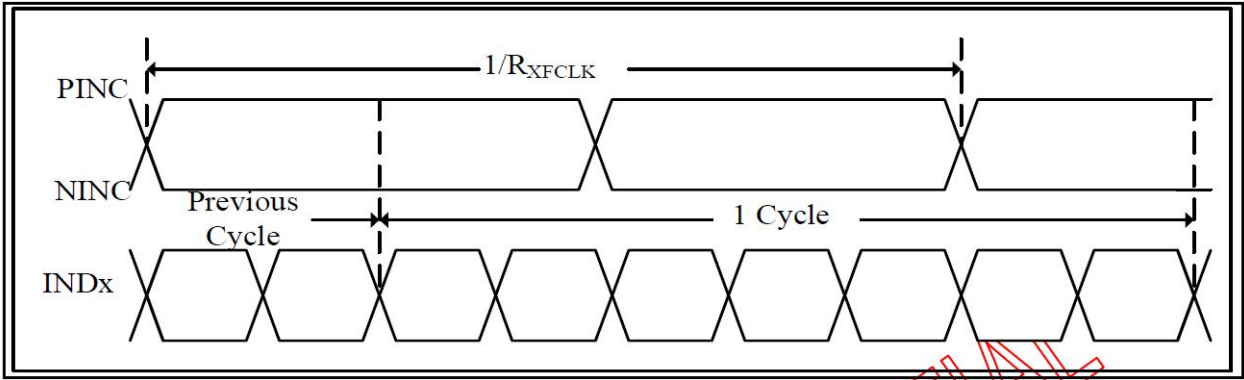
6-bit LVDS Input Timing chart



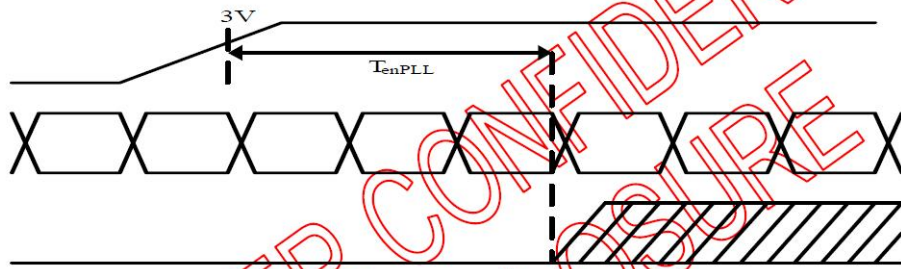
8-bit LVDS Input Timing chart

7.2. AC Characteristics

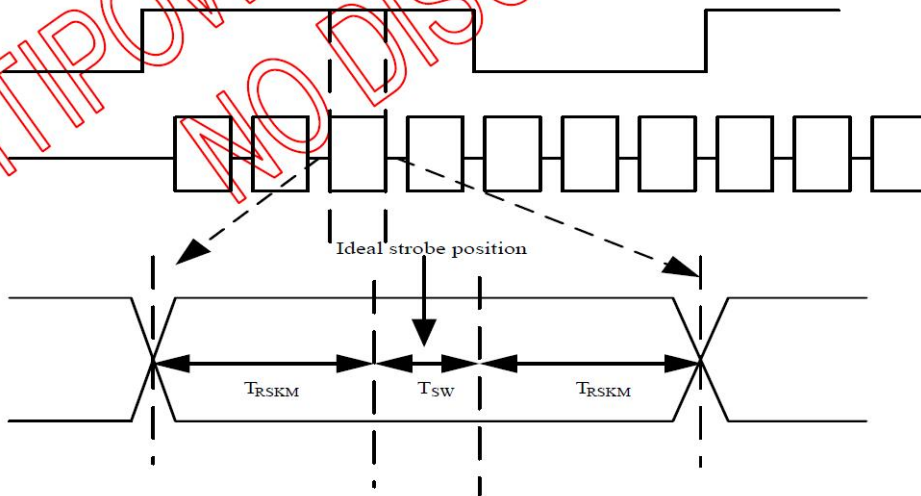
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Clock Frequency	RxFCLK		20	-	71	MHz
Input data skew margin	TRSKM	V _{DD} =400mV R _{xVCM} =1.2V RxFCLK=71MHz	500			ps
Clock High Time	TLVCH			4/(7* RxFCLK)		ns
						ns
Clock Low Time	TLVCL			3/(7* RxFCLK)		ns
PLL wake-up-time	TenPLL				150	us



LVDS timing(1)



LVDS timing(2)



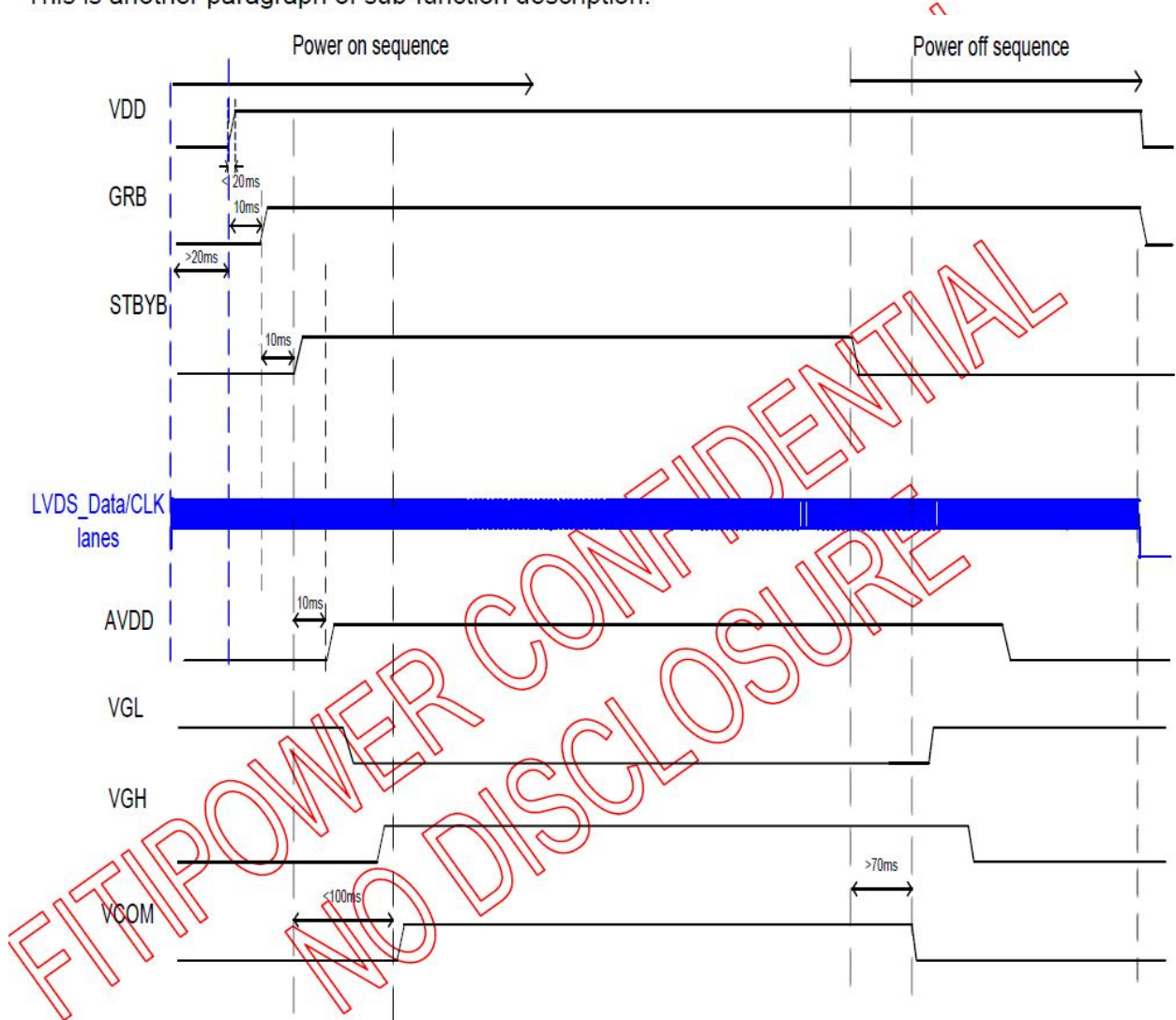
T_{sw} :Receiver strobe position
 T_{RSKM} :Receiver strobe margin

LVDS timing(3)

7.3. Power ON/OFF Sequence

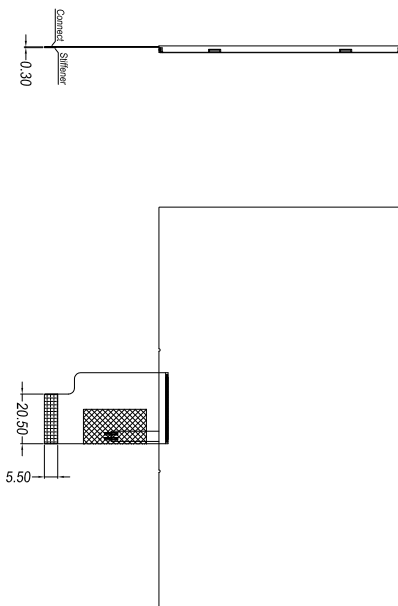
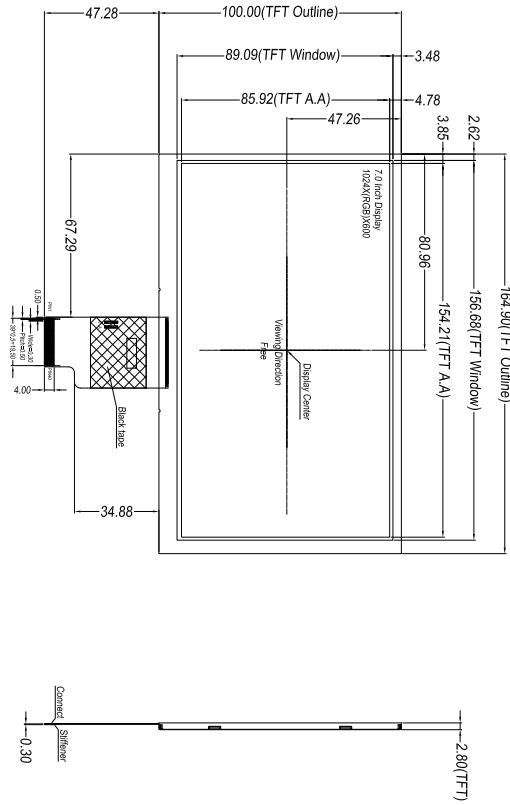
In order to prevent IC from power on reset fail, the rising time (TPOR) of the digital power supply VDD should be maintained within the given specifications. Refer to "AC Characteristics" for more detail on timing.

This is another paragraph of sub-function description.



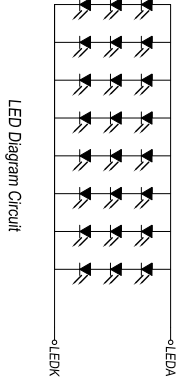
8. Outline Dimension

Display Type	Transmissive, Normally Black
Display Resolution	1024(RGB)X600
Viewing Angle	Free
Controller/Driver	/
Driving Voltage	3.3V
Operation Temperature	-20°C TO 70°C
Storage Temperature	-30°C TO 80°C
Backlight Speciality	24PCS LEDs, V _F =9.6V, I _F =160mA
Remark	Luminance: 350cd/m ² All materials in the drawing comply with the ROHS



TFT ASSIGNMENT:

PIN	SYMBOL
1	VCCM
2	VDD
3	VDD
4	VDD
5	RESET
6	STRB
7	SD15
8	FRM15
9	FRM15
10	FRM15
11	FRM15
12	FRM15
13	FRM15
14	FRM15
15	FRM15
16	FRM15
17	FRM15
18	FRM15
19	FRM15
20	FRM15
21	FRM15
22	FRM15
23	NC
24	NC
25	FRM15
26	NC
27	NC
28	SELB
29	AVDD
30	GND
31	LED-
32	LED-
33	LRS
34	LUD
35	VGL
36	NC
37	NC
38	VGH
39	LED+
40	LED+



TITLE:	LCM Outline		
PROJECT NO:	RK070CU01		
DESCRIPTION:	7.0 Inch TFT with LVDS Interface		
GENERAL TOLERANCE:	±0.3		
Rocktech Displays Limited			
THIRD ANGLE PROJECTION			
	NAME	SIGN	DATE
DRAWN	Marvin Zhou		2016.05.12
CHECKED	Marvin Zhou		2016.05.12
REV: 1.0	UNIT: MM	SCALE: 1/1	SHEET: 1 OF 1

ISSUE	MODIFY DESCRIPTION	DATE
1.0	First Issue	2016.05.12

9. Reliability and Inspection Standard

No.	Test Item		Test Conditions	Remark
1	High Temperature	Storage	80°C, 120Hr	Note
		Operation	70°C, 120Hr	Note
2	Low Temperature	Storage	-30°C, 120Hr	Note
		Operation	-20°C, 120Hr	
3	High Temperature and High Humidity		40°C, 90%RH, 120Hr	Note
4	Thermal Cycling Test(No operation)		-20°C for 30min, 70°C for 30 min. 100 cycles. Then test at room temperature after 1 hour	Note
5	Vibration Test(No operation)		Frequency :10~55 HZ; Stroke :1.5 mm;Sweep:10HZ~55HZ~10HZ; 2hours for each direction of X, Y, Z(6 hours for total)	
6	Package Drop Test		Height:60 cm,1 corner, 3 edges, 6 surfaces	
7	Electro Static Discharge		±2KV,Human Body Mode, 100pF/1500Ω	

Note:

- 1) Sample quantity for each test item is 5~10pcs.
- 2) Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

10. PRECAUTIONS FOR USING LCD MODULES

Handling Precautions

- (1) The display panel is made of glass and polarizer. As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
- (2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
- (3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
- (4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
- (5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
 - Isopropyl alcohol
 - Ethyl alcoholDo not scrub hard to avoid damaging the display surface.
- (6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
 - Water
 - Ketone
 - Aromatic solventsWipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.
- (7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
- (8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
- (9) Do not attempt to disassemble or process the LCD module.
- (10) NC terminal should be open. Do not connect anything.
- (11) If the logic circuit power is off, do not apply the input signals.
- (12) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
 - Do not alter, modify or change the shape of the tab on the metal frame.
 - Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
 - Do not damage or modify the pattern writing on the printed circuit board.
 - Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal

connector.

- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

Storage Precautions

When storing the LCD modules, the following precaution is necessary.

- (1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
- (2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
- (3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.

If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.