Rocktech Displays Limited

LCD Module Specification

Module P/N: RK050IR43

Version: 2.0

Description : 5.0 inch TFT 800*480 Pixels
With LED backlight

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Revision History

Date	Rev.	Page	Description
2015-01-11	1.0	All	First issue
2016-09-13	2.0	11-20	Modify Timing characteristics and Outline

CONTENTS

1. General Features

Item	Spec	Remark
Display Mode	Normally White transmissive	
Viewing Angle	12 O'CLOCK	
Input Signals	RGB 24 bit	
Outside Dimensions	120.7 (W) x75.8(H) x3.0(D)	
Active Area	108.0 mm(W)×64.8 mm(H)	
Number of Pixels	800(RGB)×480	
Dot Pitch	0.135 mm(H）×0. 045 mm(W)	
Pixel Arrangement	RGB Vertical stripes	
Drive IC	EK9713CA+EK73002A	

2. Absolute Maximum Ratings

The following are maximum values which, if exceeded may cause operation or damage to the unit.

ITEM	Sym.	Min.	Typ.	Max.	Unit	Remark
Power for Circuit Driving	Vdd	-0.3	-	5	V	
Backlight Forward Current	I_{LED}	-	-	25	mA	For each LED
Storage Temperature	T_{ST}	-30	-	80	℃	
Operating Ambient Humidity	H_{OP}	10	-		%RH	
Operating Ambient temperature	T_{OP}	-20	-	70	℃	

3. Electrical Specification

3.1 Driving TFT LCD Panel

Item		Sym.	Min	Typ.	Max	Unit	Note
Power for Circuit Driving		VDD	3.0	3.3	3.6	V	
Logic Input Voltage	Low Voltage	V_{IL}	0	-	0.3Vdd	V	
	High Voltage	V_{IH}	0.7Vdd	-	Vdd	V	
Logic Output Voltage	Low Voltage	V_{OL}	-	-	GND+0.4	V	
	High Voltage	V_{OH}	Vdd-0.4	-	-	V	
Power Consumption	Black Mode	P_b	T.B.D	T.B.D	T.B.D	mW	
	Standby Mode	P_w	T.B.D	T.B.D	T.B.D	mW	

3.2 Driving Backlight

Item	Sym.	Min	Typ.	Max	Unit	Note
Backlight driving voltage	V_F	18	19.2	20.4	V	
Backlight driving current	I_F	-	40	-	mA	
Backlight Power Consumption	W_{BL}	-	768	-	mW	
Life Time	-	-	30,000	-		Note 3

Note 1: (Unless specified, the ambient temperature Ta=25℃)
Note 2: The recommended operating conditions refer to a range in which operation of this product is guaranteed. Should this range is exceeded, the operation cannot be guaranteed even if the values may be without the absolute maximum ratings.
Note 3: If LED is driven by high current, high ambient temperature & humidity condition. The life time of LED will be reduced. Operating life means brightness goes down to 50% initial brightness. Typical operating life time is estimated data.

4.Optical Specifications

Optical characteristics are determined after the unit has been 'ON' and stable for approximately 30 minutes in a dark environment at 25 ℃. The values specified are at an approximate distance 500mm from the LCD surface at a viewing angle of Φ and θ equal to 0°.

Item	Sym.	Values			Unit	Note
		Min.	Typ.	Max.		
1)Contrast Ratio	C/R	-	600	-		FIG.1
2)Module Luminance	L	-	350	-	cd/m^2	FIG.1
3)Response time	Tr+Tf	-	20	-	ms	FIG.2
4)Viewing Angle	θ_T	60	70	-	Degree	FIG.3
	θ_B	40	50	-		
	θ_L	60	70	-		
	θ_R	60	70	-		
5)Chromaticity	Wx	0.274	0.316	0.358		
	Wy	0.294	0.336	0.378		
	Rx	-	-	-		
	Ry	-	-	-		
	Gx	-	-	-		
	Gy	-	-	-		
	Bx	-	-	-		
	By	-	-	-		

◆ Measurement System

Notes:

1. Contrast Ratio(CR) is defined mathematically as :

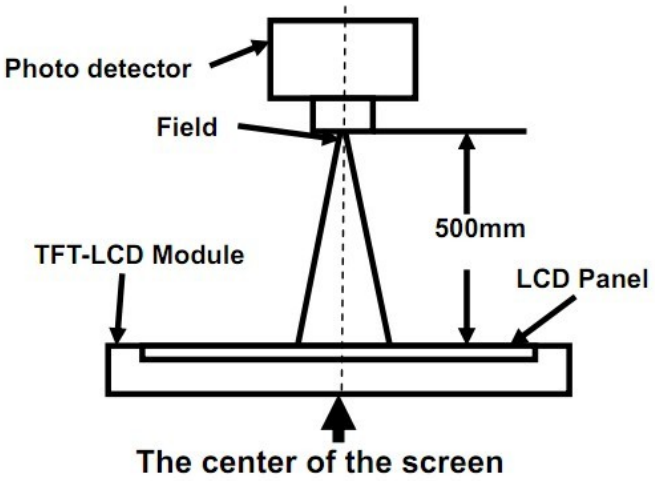
$$\text{Contrast Ratio} = \frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$$

2. Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white. For more information see FIG 1.

3. Response time is the time required for the display to transition from white to black (Rising Time, Tr) and from black to white (Falling Time, Tf). For additional information see FIG 2.

4. Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

FIG. 1 Optical Characteristic Measurement Equipment and Method



Item	Photo detector	Field
Contrast Ratio	SR-3A	1°
Luminance		
Chromaticity		
Lum Uniformity		
Response Time	BM-7A	2°

FIG. 2 The definition of Response Time

The response time is defined as the following figure and shall be measured by switching the input signal for “black” and “white”.

Response Time = Rising Time(Tr) + Falling Time(Tf)

- Rising Time(Tr) : Full White 90% → Full White 10% Transmittance.
- Falling Time(Tf) : Full White 10% → Full White 90% Transmittance.

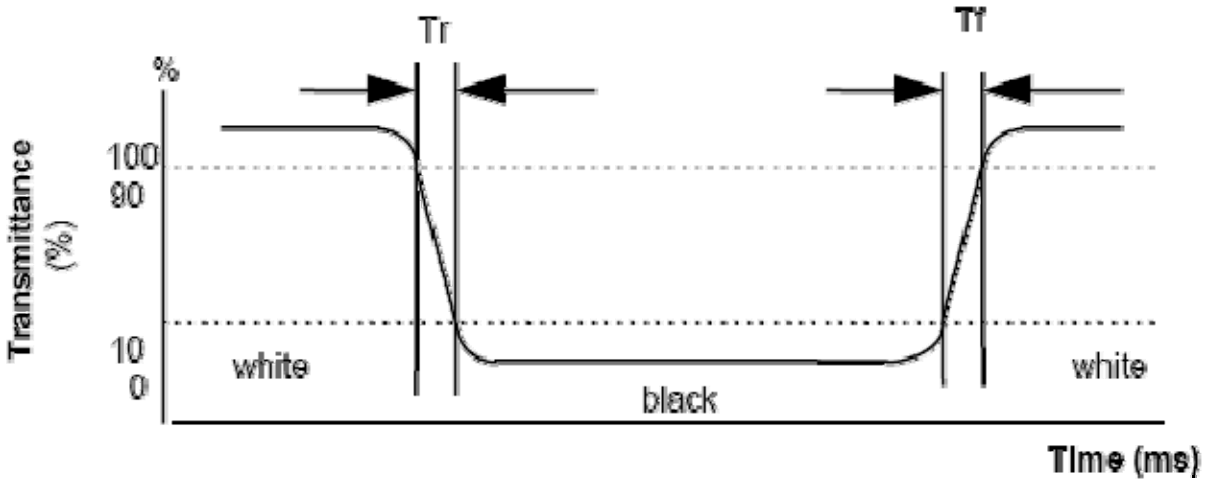
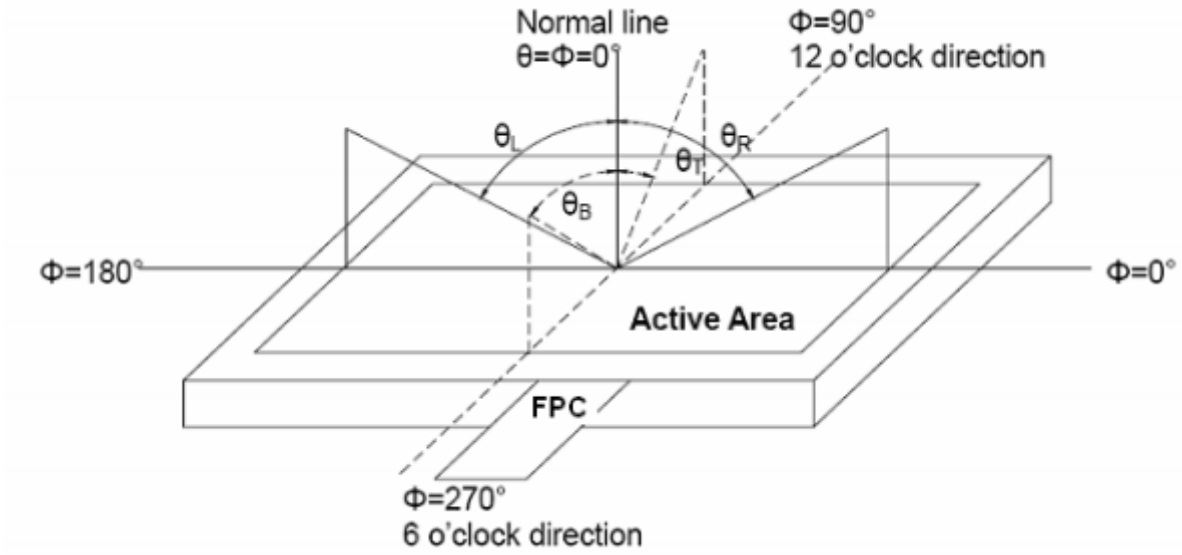
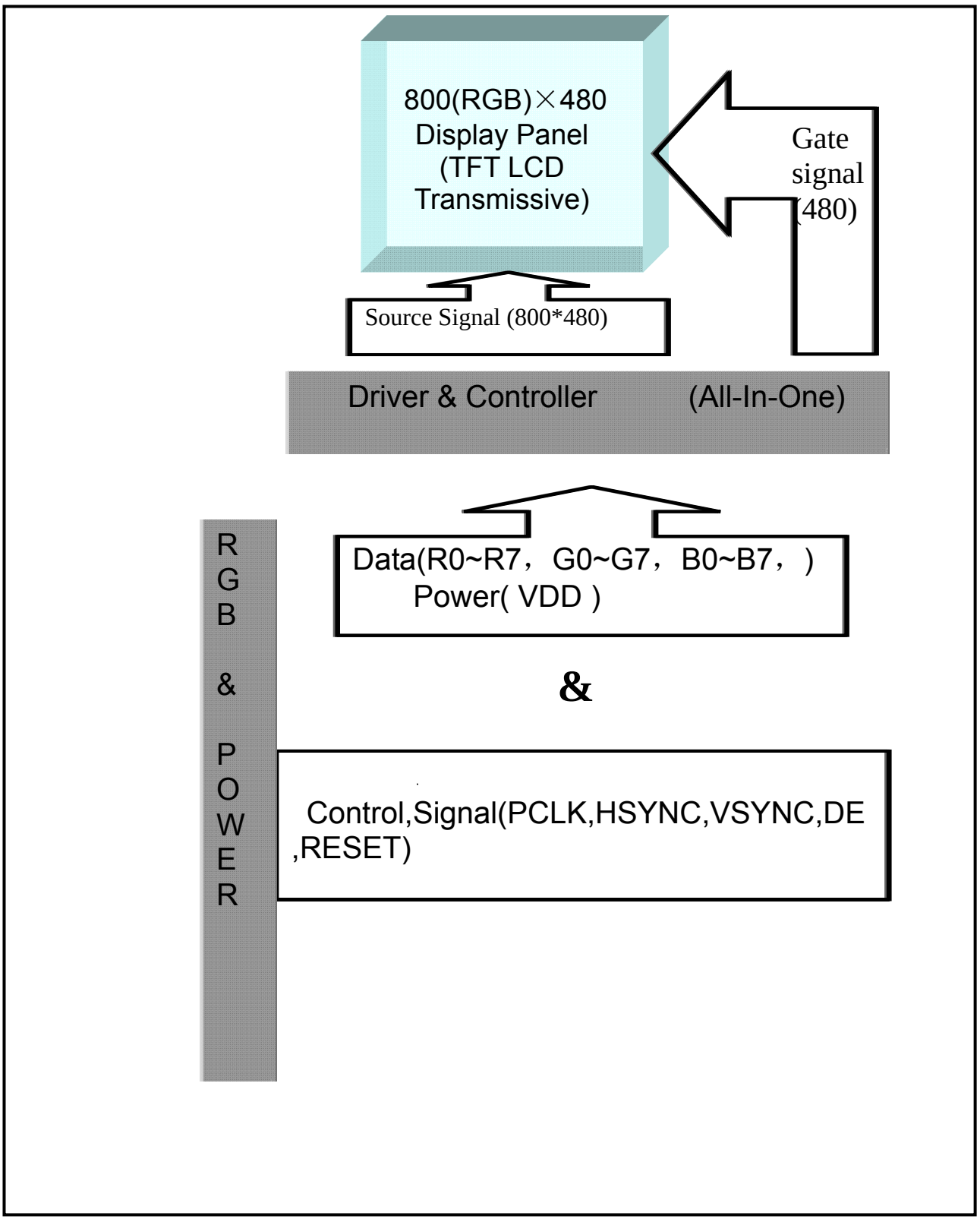


FIG. 3 The definition of Viewing Angle

Use Fig. 1(Test Procedure) under Measurement System to measure the contrast from the measuring direction specified by the conditions as the following figure.



5.Block Diagram



6.Pin Description

Item	Terminal	Functions
1	VLED-	B/L Power input PIN negative
2	VLED+	B/L Power input PIN anode
3	GND	Ground
4	VDD	Power supply
5--12	R0--R7	Display for R dot
13--20	G0--G7	Display for G dot
21--28	B0--B7	Display for B dot
29	GND	Ground
30	DCLK	Clock for input data
31	DISP	Display on/off control
32	HSYNC	Horizontal synchronizing signal
33	VSYNC	Vertical synchronizing signal
34	DE	Data input enable
35	NC	No connect
36	GND	Ground
37	NC/XR	No connect
38	NC/YD	No connect
39	NC/XL	No connect
40	NC/YU	No connect

7.Timing Characteristics

7.1 Input Setup Timing setting

7.1.1 Input Setup Timing setting

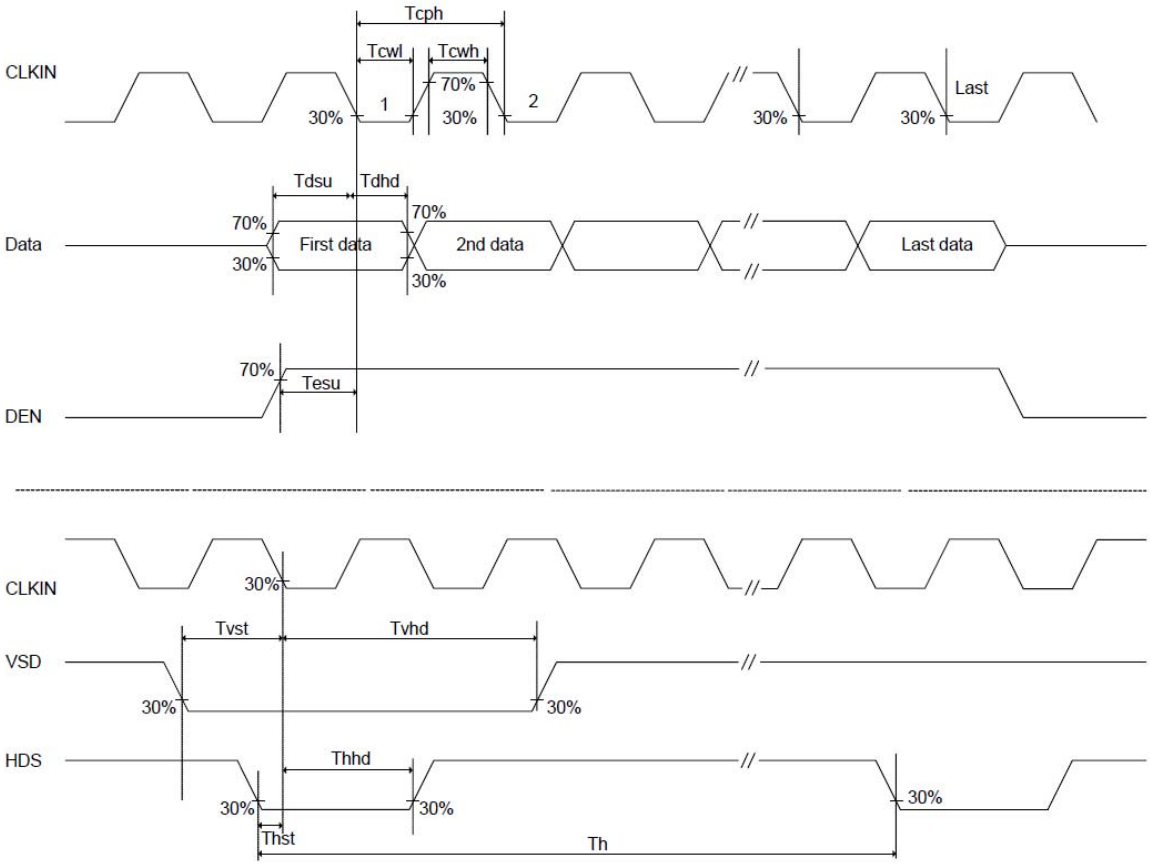
(TA = -20 to 85°C, VDD = 1.8 to 3.6V, AVDD = 6.5 to 13.5V, GND = AVSS = 0V)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
VDD Power On Slew rate	TPOR	From 0V to 90% VDD	-	-	20	ms
RSTB pulse width	TRST	CLKIN = 40MHz	1	-	-	ms
CLKIN cycle time	Tcph	-	20	-	-	ns
CLKIN pulse duty	Tcwh	-	40	50	60	%
VSD setup time	Tvst	-	8	-	-	ns
VSD hold time	Tvhd	-	8	-	-	ns
HSD setup time	Thst	-	8	-	-	ns
HSD hold time	Thhd	-	8	-	-	ns
Data set-up time	Tdsu	D0[7:0], D1[7:0], D2[7:0] to CLKIN	8	-	-	ns
Data hold time	Tdhd	D0[7:0], D1[7:0], D2[7:0] to CLKIN	8	-	-	ns
DEN setup time	Tesu	-	8	-	-	ns
DEN hold time	Tehd	-	8	-	-	ns
Output stable time	Tsst	10% to 90% target voltage. CL=120pF, R=10K ohm	-	-	6	us

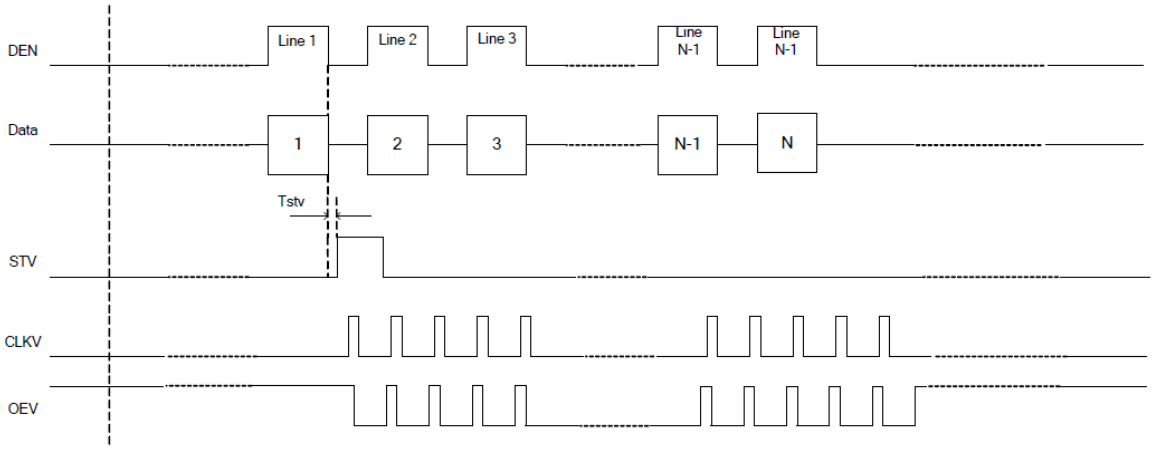
Parallel 24-bit RGB Mode

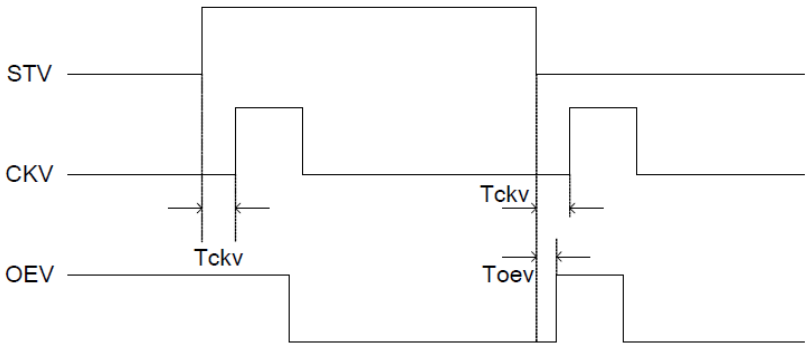
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
CLKIN Frequency	Fclk	VDD = 1.8V ~3.6V	-	33.3	50	MHz
CLKIN Cycle Time	Tclk	-	20	30	-	ns
CLKIN Pulse Duty	Tcwh	Tclk= Tcwh + cwl	40	50	60	%
	Tcwl		40	50	-60	%
VSD to STV	Tstv	HV mode	-	24	-	H
DEN to STV	Tstv	DE mode	-	4	-	CLKIN
STV pulse width	Twstv	-	-	0.5	-	H
STV to CKV	Tckv	-	-	18	-	CLKIN
STV to OEV	Toev	-	-	2	-	CLKIN
CKV Pulse Width	Twckv	-	-	66	-	CLKIN
OEV Pulse Width	Twoev	-	-	50	-	CLKIN

7.1.2 Input Clock and Data Timing Diagram

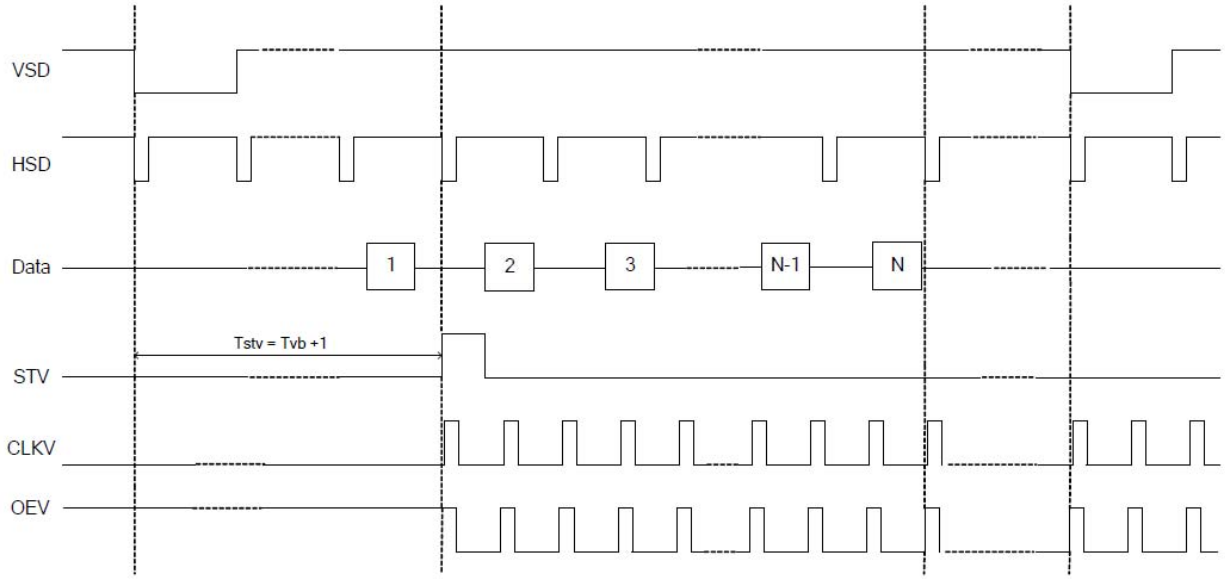


7.1.3 Vertical Timing Diagram DE





7.1.4 Vertical Timing Diagram HV



7.2 Data Input Format

7.2.1 Data Input Timing Parameter Setting

Horizontal input timing

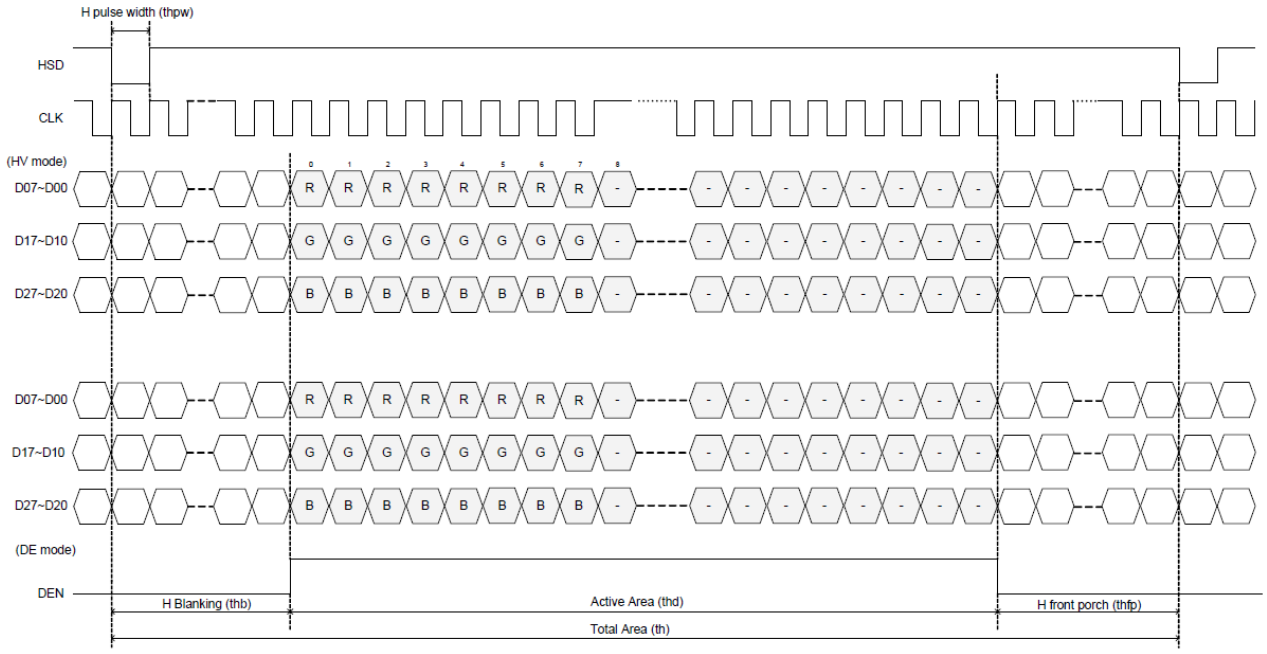
Parameter		Symbol	Value			Unit
Horizontal display area		thd		800		DCLK
DCLK frequency		fclk	Min.	Typ.	Max	
			-	33.3	50	MHz
1 Horizontal Line		th	862	1056	1200	DCLK
HSD pulse width	Min.	thpw		1		
	Max.			40		
HSD Back Porch (Blanking)		thb	46	46	46	
HSD Front Porch		thfp	16	210	354	

Vertical input timing

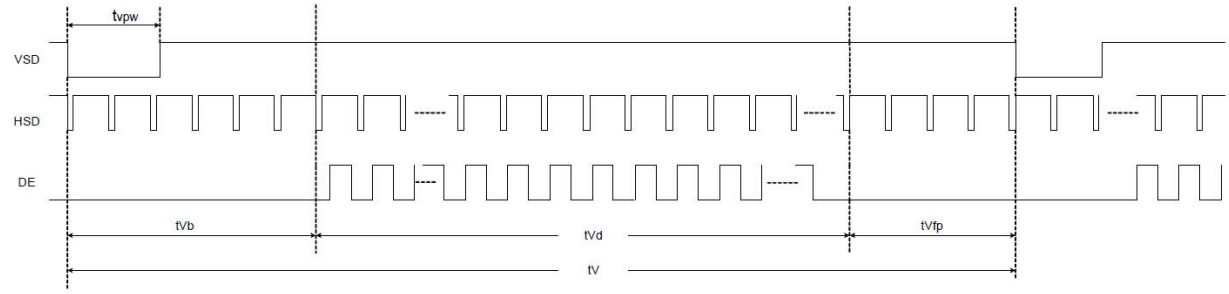
Parameter	Symbol	Min.	Typ.	Max.	Unit
Vertical display area	tvd		480		H
VSD period time	tv	510	525	650	H
VSD pulse width	tvpw	1	-	20	H
VSD Back Porch (Blanking)	tvb	23	23	23	H
VSD Front Porch	tvfp	7	22	147	H

7.2.2 Data Input Timing Diagram

7.2.2.1 Horizontal Input Timing Diagram



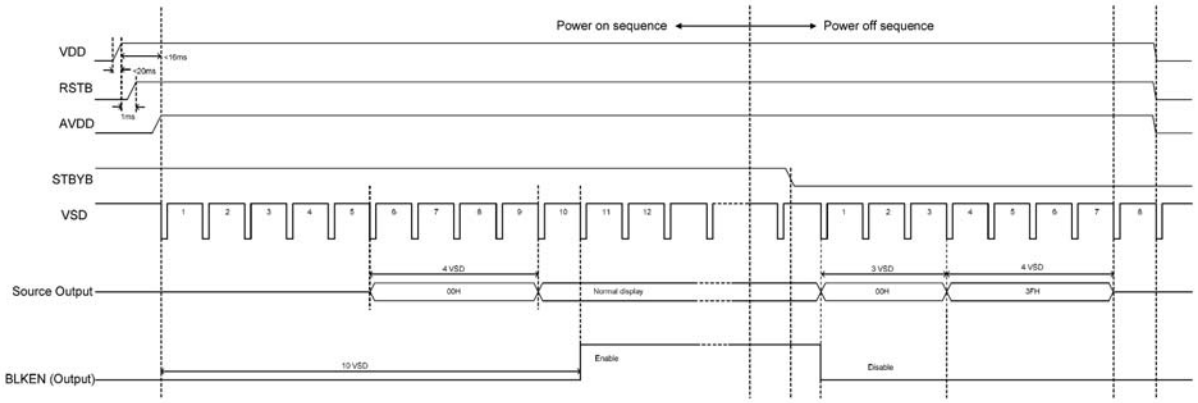
7.2.2.2 Vertical Input Timing Diagram



7.3 Power on/off Sequence

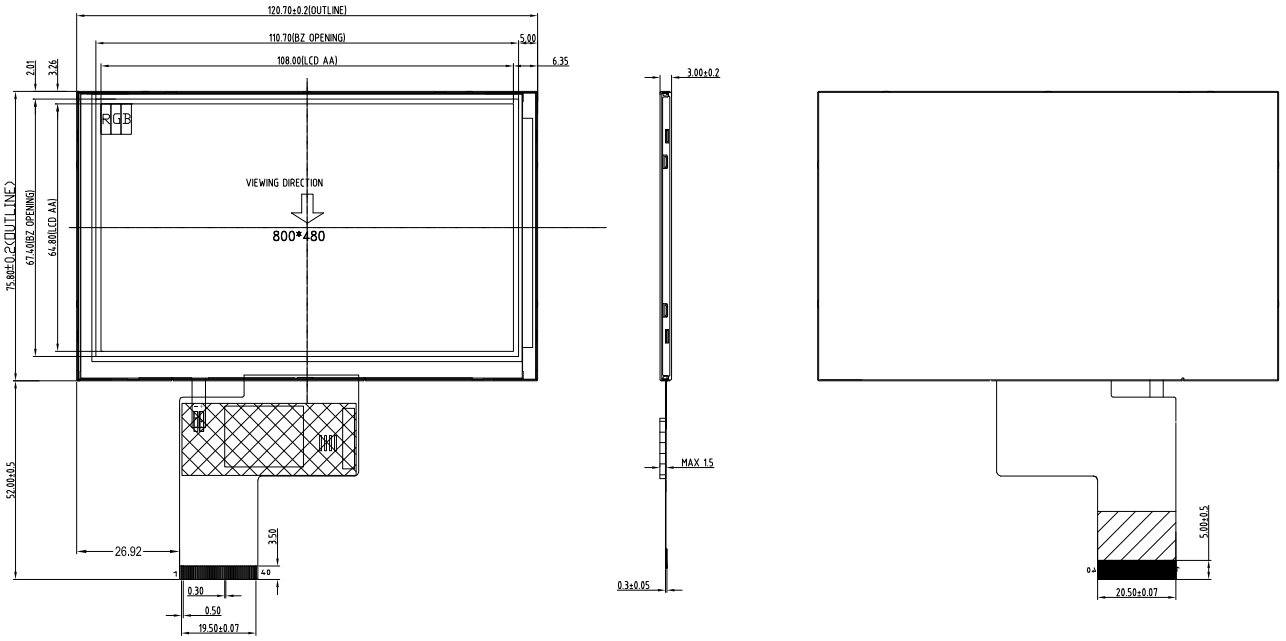
In order to prevent IC from power on reset fail, the rising time (TPOR) of the digital power supply VDD should be maintained within the given specifications. Refer to “AC Characteristics” for more detail on timing.

This is another paragraph of sub-function description.



8.Outline Dimension

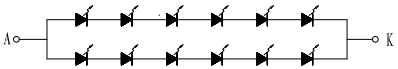
ISSUE	MODIFY DESCRIPTION	DATE
1.0	First Issue	2015.12.26
2.0	Update Backlight	2016.01.09
3.0	Modify driving IC	2016.09.13



TFT ASSIGNMENT:

PIN	SYMBLE
1	VLED-
2	VLED+
3	GND
4	VDD
5	R0
6	R1
7	R2
8	R3
9	R4
10	R5
11	R6
12	R7
13	G0
14	G1
15	G2
16	G3
17	G4
18	G5
19	G6
20	G7
21	B0
22	B1
23	B2
24	B3
25	B4
26	B5
27	B6
28	B7
29	GND
30	DCLK
31	DISP
32	HSYNC
33	VSYNC
34	DE
35	NC
36	GND
37	NC/XR
38	NC/YD
39	NC/XL
40	NC/YU

Display Type	Transmissive, Normally White
Display Resolution	800(RGB)*480
Viewing Angle	12:00
LCD Controller/Driver	EK9713CA(TFT)+EK73002A(TFT)
LCD Driving Voltage	3.3V
Operation Temperature	-20°CTO 70°C
Storage Temperature	-30°C TO 80°C
Backlight Speciality	LED Side(White, 12PCS DIES),Vf=19.2V,If=40mA
Remark	All materials in the drawing comply with the ROHS



CIRCUIT DIAGRAM

TITLE:	LCM Outline
PROJECT NO:	RK050IR43
DESCRIPTION:	5.0 Inch TFT
General Tolerance: ±0.2	

Rocktech Displays Limited

THIRD ANGLE PROJECTION

	NAME	SIGN	DATE
DRAWN	Marvin Zhou		2016.09.13
CHECKED	Marvin Zhou		2016.09.13
REV: 3.0	UNIT: mm	SCALE:1/1	SHEET: 1 OF 1

9. Reliability and Inspection Standard

No.	Test Item		Test Conditions	Remark
1	High Temperature	Storage	80℃, 120Hr	Note
		Operation	70℃, 120Hr	Note
2	Low Temperature	Storage	-30℃, 120Hr	Note
		Operation	-20℃, 120Hr	
3	High Temperature and High Humidity		40℃, 90%RH, 120Hr	Note
4	Peeling Off (Storage)		≧500gf/cm	Note
5	FPC Bending Test		≧6,000 times, 2/sec	Note
6	Vibration Test(Storage)		50HZ, 30min, Amplitude: 2 cm, X/Y/Z directions	Note
7	Drop Test		60cm/ 3Corner/ 8Face, 1Cycle	Note

Note:

1) The test samples should be applied to only one test item.
2) Sample size for each test item is 5~10pcs.
3) For Damp Proof Test, pure water(Resistance>1MΩ) should be used.
4) In case of malfunction defect caused by ESD damage, if it would be recovered to normal state after resetting, it would be judged as a good part.
5) EL evaluation should be excepted from reliability test with humidity and temperature: Some defects such as black spot/blemish can happen by natural chemical reaction with humidity and fluorescence EL has.
6) After the reliability test, the test samples should be inspected after 2 hours at least.
7) Functional test is OK. Missing segment, shorts, unclear segment, non display, display abnormally, liquid crystal leak are not allowed.
8) After testing, the current Idd should be within initial value ±20%.
9) No low temperature bubbles ,end seal loose and fall, frame rainbow, ACF bubble growing are allowable in the appearance test.

10.PRECAUTIONS FOR USING LCD MODULES

Handing Precautions

(1) The display panel is made of glass and polarizer. As glass is fragile, it tends to become or chipped during handling especially on the edges. Please avoid dropping or jarring. Do not subject it to a mechanical shock by dropping it or impact.
(2) If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth. If the substance contacts your skin or clothes, wash it off using soap and water.
(3) Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary. Do not touch the display with bare hands. This will stain the display area and degraded insulation between terminals (some cosmetics are determined to the polarizer).
(4) The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully. Do not touch, push or rub the exposed polarizers with anything harder than an HB pencil lead (glass, tweezers, etc.). Do not put or attach anything on the display area to avoid leaving marks on. Condensation on the surface and contact with terminals due to cold will damage, stain or dirty the polarizer. After products are tested at low temperature they must be warmed up in a container before coming is contacting with room temperature air.
(5) If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten cloth with one of the following solvents
- Isopropyl alcohol
- Ethyl alcohol
Do not scrub hard to avoid damaging the display surface.
(6) Solvents other than those above-mentioned may damage the polarizer. Especially, do not use the following.
- Water
- Ketone
- Aromatic solvents
Wipe off saliva or water drops immediately, contact with water over a long period of time may cause deformation or color fading. Avoid contacting oil and fats.
(7) Exercise care to minimize corrosion of the electrode. Corrosion of the electrodes is accelerated by water droplets, moisture condensation or a current flow in a high-humidity environment.
(8) Install the LCD Module by using the mounting holes. When mounting the LCD module make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
(9) Do not attempt to disassemble or process the LCD module.
(10) NC terminal should be open. Do not connect anything.
(11) If the logic circuit power is off, do not apply the input signals.
(12) Since LCM has been assembled and adjusted with a high degree of precision, avoid applying excessive shocks to the module or making any alterations or modifications to it.
- Do not alter, modify or change the shape of the tab on the metal frame.
- Do not make extra holes on the printed circuit board, modify its shape or change the positions of components to be attached.
- Do not damage or modify the pattern writing on the printed circuit board.
- Absolutely do not modify the zebra rubber strip (conductive rubber) or heat seal

connector.
- Except for soldering the interface, do not make any alterations or modifications with a soldering iron.
- Do not drop, bend or twist LCM.

Storage Precautions

When storing the LCD modules, the following precaution is necessary.
(1) Store them in a sealed polyethylene bag. If properly sealed, there is no need for the dessicant.
(2) Store them in a dark place. Do not expose to sunlight or fluorescent light, keep the temperature between 0°C and 35°C.
(3) The polarizer surface should not come in contact with any other objects. (We advise you to store them in the container in which they were shipped).

Others

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.
If the LCD modules have been operating for a long time showing the same display patterns, the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be regained by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.
To minimize the performance degradation of the LCD modules resulting from destruction caused by static electricity etc., exercise care to avoid holding the following sections when handling the modules.
- Exposed area of the printed circuit board.
-Terminal electrode sections.